

IFW/2123



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February 10, 2005

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Re: U.S. Patent Application No.: 10/040,709
For: *AUTOMATED APPROACH FOR MEASURING SIGNALING SETUP, HOLD AND JITTER*
Inventors: William B. Gist, *et al.*
Filed: January 7, 2002
Our Ref. No.: SMQ-044

Dear Sir:

I enclose herewith for filing in the above-identified application the following:

1. Information Disclosure Statement;
2. PTO Form SB/08;
3. Copies of references cited in PTO Form SB/08 (2);
4. A Return Postcard.

No additional costs are believed to be due in connection with the filing of this Information Disclosure Statement. However, please charge any necessary fees in connection with the enclosed statement to our Deposit Order Account No. 12-0080. For this purpose, a duplicate of this sheet is attached.

I hereby certify that this correspondence is deposited with the United States Postal Service as first class mail in an envelope addressed to:
MS Amendment, Commissioner for Patents, P.O. Box 1450,
Alexandria, Virginia 22313-1450 on:

February 10, 2005

Date

Kevin J. Canning, Esq. Registration No. 35,470

Respectfully submitted,
LAHIVE & COCKFIELD, LLP

Kevin J. Canning, Esq.
Registration No. 35,470
Attorney for Applicants



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: William B. Gist, *et al.*

Serial No.: 10/040,709

Filed: January 7, 2002

For: *AUTOMATED APPROACH FOR MEASURING SIGNALING SETUP, HOLD AND JITTER*

Attorney Docket No.: SMQ-044

Group Art Unit: 2123

Examiner: Not Yet Assigned

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

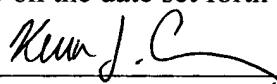
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February 10, 2005

Date of Signature and of Mail Deposit

By:


Kevin J. Canning, Esq.
Registration No. 35,470
Attorney for Applicants

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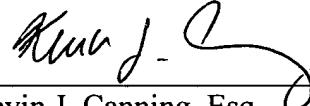
Dear Sir:

Applicants and their Attorney are aware of the following publications and information, listed on the attached PTO Form SB/08, and in accordance with 37 CFR §1.97 hereby submit these publications for the Examiner's consideration. A copy of each cited publication is enclosed.

This statement is not to be interpreted as a representation that the cited publications are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such publication is prior art. Moreover, Applicants understand that the Examiner will make an independent evaluation of the cited publications.

Under 37 CFR § 1.97(b)(3), no additional costs are believed to be due in connection with the filing of this disclosure. If, however, a first Office Action on the merits issues in this application bearing a mailing date prior to the date of this Information Disclosure Statement, please charge the appropriate fee as required under 37 CFR §1.17(p) to our Deposit Order Account No. 12-0080.

Respectfully submitted,
LAHIVE & COCKFIELD, LLP



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Date: February 10, 2005

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Enclosures



PTO/SB/08a/b (08-03)

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Substitute for form 1449A/B/PTO				<i>Complete if Known</i>	
				Application Number	10/040,709
				Filing Date	January 7, 2002
				First Named Inventor	William B. Gist
				Art Unit	2123
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	SMQ-044

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS					
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			²
	A1	Sepcctrat Signal Explorer – Pre and Post Route Signal Integrity Analysis. 2000, Cadence Design Systems, Inc., 3 pages.			
	A2	Specctrat SI Expert – High Speed System Level Design and Analysis. 2000, Cadence Design Systems, Inc., 11 pages.			

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